

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bedichek, et al. Confirmation No.: 7303  
Serial No. : 09/417,332 Group Art Unit: 2183  
Filed : 10/13/1999 Examiner: Ellis, R.  
For : METHOD FOR INTEGRATION OF INTERPRETATION AND  
TRANSLATION IN A MICROPROCESSOR

RESPONSE TO NON-COMPLIANT AMENDMENT

Commissioner for Patents & Trademarks  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Non-Compliance mailed 5/28/2008, the Applicants respectfully re-submit a copy of the previous response filed on April 3, 2008. In the above captioned patent application, please consider the following remarks: